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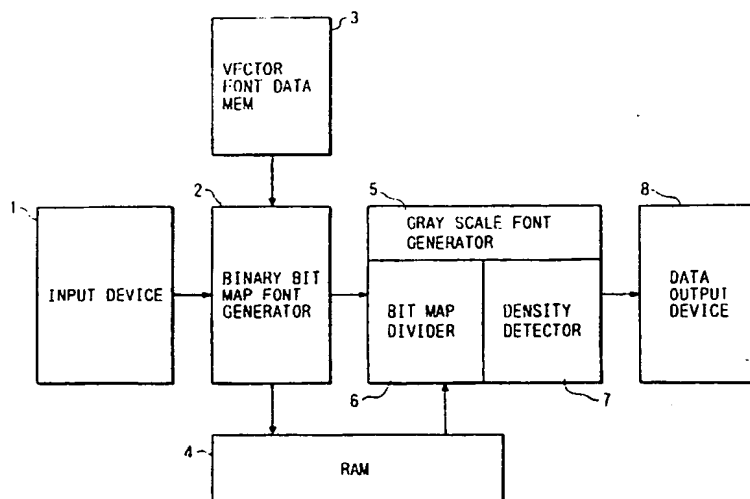
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(54) **Pattern generation method and pattern generation apparatus.**

(57) In a pattern generation method, corresponding binary bit map data is generated based on desired one of vector font data, an area of the binary bit map data on a memory is divided to judge a density in a predetermined area, and digital multi-value data is

generated in correspondence with the judged density. There is also disclosed a pattern generation apparatus for carrying out this method.

FIG. 1



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PATTERN GENERATION METHOD AND PATTERN GENERATION APPARATUS

BACKGROUND OF THE INVENTION

Field of the Invention

The present invention relates to a pattern generation method and apparatus which can be applied to an output device capable of expressing gradation.

Related Background Art

When a gray scale font which can express gradation is assumed, it can be stored in a character generator in a bit map format, i.e., in a format that multi-value data is added to one dot.

However, when font data is stored in the bit map format, the following problems are posed.

- (1) A font cannot be enlarged or reduced to an arbitrary size.
- (2) Since multi-value data is stored in the bit map format, a data size is increased, and a memory having a large capacity is required for storing fonts.

SUMMARY OF THE INVENTION

According to the present invention, a font such as a vector font which can be arbitrarily enlarged or reduced can be expressed by gray scale levels by utilizing binary pattern data without increasing a memory capacity necessary for fonts.

It is an object of the present invention to provide a pattern generation apparatus which converts a vector font into a binary bit map pattern, and generates, based on the binary bit map pattern, digital multi-value data in correspondence with a density based on a bit pattern distribution.

It is another object of the present invention to provide a pattern generation method and apparatus, for generating multi-value data on the basis of a vector font, converting the multi-value data into an analog image signal, and comparing the analog image signal with a triangular wave having a predetermined period to pulse-width modulate it, thereby outputting a gradation pattern.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a block diagram of a character generator according to an embodiment of the present invention;

Figs. 2A to 2C are views showing a procedure for generating a gray scale font according to the present invention;

Figs. 3 and 4 are flow charts showing control sequences of the present invention; and

Fig. 5 is a sectional view for explaining an arrangement of an output apparatus.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Fig. 1 best illustrates the characteristic feature of the present invention. In Fig. 1, an input device 1 receives an instruction to a character generator. A binary bit map font generator 2 generates a bit map font on the basis of vector data. A vector font data memory 3 stores vector font data of characters to be generated by the binary bit map font generator 2. A RAM 4 stores the bit map font data generated by the binary bit map font generator 2, and supplies the bit map data to a gray scale font generator 5. The gray scale font generator 5 generates a gray scale font on the basis of the bit map data generated by the binary bit map font generator 2. A bit map divider 6 divides the bit map data according to gray scale levels. A density detector 7 detects the densities of areas divided by the bit map divider 6. A data output device 8 supplies the densities detected by the detector 7 to an output apparatus comprising the character generator shown in Fig. 1. In general, the character generator is assembled in a printer or a display device.

Figs. 2A to 2C show a procedure of operations of the character generator according to the present invention. Fig. 2A illustrates bit map font data generated by the binary bit map font generator 2, and a binary character of a bit map is generated based on a vector font. In this case, in order to obtain a necessary gray scale level, a character is generated with a resolution higher than an output resolution. When the size (resolution) of a character on a memory is set to be $m \times n$, gradation of $m \times n + 1$ levels can be obtained. That is, m and n are set to satisfy a relation (output resolution of the device) $\leq m \times n + 1$. In this embodiment, since 25%, 50%, and 75% gray levels are adopted, five gray levels are available together with 0% and 100% levels, and $m = n = 2$ is set. Therefore, a character is generated at a x2 resolution in both vertical and horizontal directions. Fig. 2B shows a bit map divided by the bit map divider. Since a character has x2 resolutions in both the vertical and horizontal directions, the character is divided in units of 2 dots (vertical direction) x 2 dots (horizontal direction) areas. Fig. 2C shows detected densities

based on the density-divided bit map. If there is one dot in a divided area this area can be expressed by a 25% gray level; if there are two dots, it can be expressed by a 50% gray level; and if there are three dots, it can be expressed by a 75% gray level. As a matter of course, if there is no dot, the area is expressed by white, and if there are four dots, it is expressed by black. Densities detected in this manner can be directly output to an apparatus comprising this character generator (e.g., a print buffer) as outputs of the character generator, e.g., digital multi-value (e.g., 4-bit) data. Thus, a memory capacity can be reduced as compared to a case wherein digital multi-value data is adopted as a font, and a high-quality output can be obtained.

Figs. 3 and 4 are flow charts showing a sequence for generating a gray scale font according to the present invention. This processing is executed by a CPU for controlling the overall output apparatus (not shown) on the basis of a program. Fig. 3 shows a sequence from the beginning to the end of character generation. In step S101 in Fig. 3, the input device 1 inputs a code of a character to be generated. In step S102, the input device 1 designates the size of a character to be generated. In step S103, a bit map font whose character size is enlarged twice in both the vertical and horizontal directions is generated by the binary bit map font generator 2 on the basis of vector font data stored in the vector font data memory 3, and is stored in the RAM 4. More specifically, original one dot is expressed by 2×2 dots, and a dot distribution in the 2×2 dot area is checked. The character size is enlarged twice in both the vertical and horizontal directions since the five gray levels, i.e., 0%, 25%, 50%, 75%, and 100% are available. If the number of gray levels is changed, an enlargement magnification is also changed. In step S104, a gray scale font is generated on the basis of the bit map font generated in step S103. Fig. 4 shows in detail the processing in step S104.

Fig. 4 is a flow chart showing a sequence for generating gray scale font data, and this processing is executed by the gray scale font generator 5. In step S110 in Fig. 4, a vertical check position YC is set to be 0. In step S111, a horizontal check position XC is set to be 0. The bit map divider 6 executes step S112 using the bit map font generated on the RAM 4. In step S112, the number of black dots included between a position (XC,YC) to a position (XC+1,YC+1) is counted. The number of black dots included between the position (XC,YC) to the position (XC+1,YC+1) can take a value falling within a range of 0 to 4. In step S113, the density detector 7 checks if the number of black dots is 0. If YES in step S113, step S114 is executed. In step S114, a gray level at the position

(XC,YC) of the gray scale font is set to be 0%. In step S115, the density detector 7 checks if the number of black dots is 1. If YES in step S115, step S116 is executed. In step S116, the gray level at the position (XC,YC) is set to be 25%. In step S117, the density detector 7 checks if the number of black dots is 2. If YES in step S117, step S118 is executed. In step S118, the gray level at the position (XC,YC) is set to be 50%. In step S119, the density detector 7 checks if the number of black dots is 3. If YES in step S119, step S120 is executed. In step S120, the gray level at the position (XC,YC) is set to be 75%. In step S121, the density detector 7 checks if the number of black dots is 4. If YES in step S121, step S122 is executed. In step S122, the gray level at the position (XC,YC) is set to be 100%. The set value is output from the data output device 8. In step S123, "2" is added to XC. In this case, a value to be added is a horizontal enlargement magnification in step S103 in Fig. 3. In step S124, it is checked if XC exceeds the width of the bit map font. If YES in step S124, step S125 is performed. If NO in step S124, step S112 is executed. In step S125, "2" is added to YC. In this case, a value to be added is a vertical enlargement magnification in step S103 in Fig. 3. In step S126, it is checked if YC exceeds the length of the bit map font. If YES in step S126, gray scale font data generation processing is ended; otherwise, step S111 is executed.

An apparatus for generating a bit map font based on a vector font has already been used in "Laser Writer (registered trademark)" (available from Apple Computer Inc.) or in "EZPS (registered trademark)" (available from CANON INC.) An apparatus of this type selectively generates bit map data according to one vector font data based on data, e.g., a character code, and is known to those who are skilled in the art. In this embodiment, a bit map font is generated based on a vector font. Alternatively, a bit map font having a predetermined size may be stored in the RAM 4 in step S103 in Fig. 3.

As described above, a gray scale font having an arbitrary size can be easily generated by a simple system.

A printer as an example of the data output device 8 shown in Fig. 1 will be briefly described below with reference to Fig. 5. A PWM (pulse-width modulation) circuit 778 converts a multi-value digital image signal (signal illustrated in Fig. 2C) from a printer I/F (not shown) into an analog image signal, and compares this analog image signal with a triangular wave having a predetermined period, thus forming a PWM signal. Note that the printer I/F includes a print buffer. The PWM signal is applied to a laser output device 711 to ON/OFF-modulate a laser, thereby forming a gradation im-

age. The laser output device or scanner 711 includes a laser output unit for converting an image signal from the PWM circuit 778 into a light signal, a polygonal (e.g., octahedral) mirror 712, a motor (not shown) for rotating this mirror 712, and an f/θ lens (focusing lens) 713, and the like. The printer also includes a reflection mirror 714 for changing the optical path of a laser beam, and a photosensitive drum 715. A laser beam emerging from the laser output unit is reflected by the polygonal mirror 712, and linearly scans (raster-scans) the surface of the photosensitive drum 715 via the lens 713 and the mirror 714, thereby forming a latent image corresponding to an original image.

The printer also includes a primary charger 717, an entire surface exposure lamp 718, a cleaner unit 723 for recovering residual toner which is not transferred, and a pre-transfer charger 724. These members are arranged around the photosensitive drum 715.

A developing unit 726 develops an electrostatic latent image formed on the surface of the photosensitive drum 715. The developing unit 726 is constituted by developing sleeves 731Y, 731M, 731C, and 731Bk which selectively contact the photosensitive drum 715 to develop the latent image, toner hoppers 730Y, 730M, 730C, and 730Bk for storing replenishing toners, and screws 732 for transferring the toners. These members are arranged around a rotational shaft P of the developing unit. For example, when a yellow toner image is to be formed, yellow toner development is performed at the illustrated position. When a magenta toner image is to be formed, the developing unit 726 is rotated about the shaft P shown in Fig. 5, so that the developing sleeve 731M in the magenta developing device is placed at a position where it can contact the photosensitive drum 715. The same applies to cyan and black development operations.

The printer further includes a transfer drum 716 for transferring a toner image formed on the photosensitive drum 715 onto a paper sheet, an actuator plate 719 for detecting a moving position of the transfer drum 716, a position sensor 720 which comes closer to the actuator plate 719 to detect that the transfer drum 716 is moved to a home position, a transfer drum cleaner 725, a sheet press roller 727, a discharger 728, and a transfer charger 729. These members 719, 720, 725, 727, and 729 are arranged around the transfer roller 716.

The printer also includes paper feed cassettes 735 and 736 for storing paper sheets, paper feed rollers 737 and 738 for feeding paper sheets from the cassettes 735 and 736, and timing rollers 739, 740, and 741. A paper sheet fed and conveyed via these rollers is guided by a paper guide 749, is wound around the transfer drum 716 while its lead-

ing end is carried by a gripper (to be described later), and is then subjected to an image formation process.

A drum motor 550 synchronously rotates the photosensitive drum 715 and the transfer drum 716. A separation pawl 750 separates a paper sheet from the transfer drum 716 after the image formation process. A conveyor belt 742 conveys the separated paper sheet. An image fixing unit 743 fixes an image formed on the paper sheet conveyed by the conveyor belt 742. The image fixing unit 743 comprises a pair of thermocompression rollers 744 and 745. As a printer for outputting a gradation image, an ink-jet printer for performing recording using an ink may be employed, and an ink dot size or the number of ink droplets may be controlled to output a gradation image.

As described above, according to the present invention, there can be provided a pattern generation apparatus comprising storage means for storing binary bit map data, judgment means for dividing an area of the bit map data stored in the storage means, and judging a density in a predetermined area, and generation means for generating corresponding digital multi-value data in accordance with the density judged by the judgment means.

As described above, according to the present invention, there can be provided a pattern generation method comprising the steps of generating corresponding binary bit map data based on desired one of vector font data, dividing an area of the binary bit map data on a memory, judging a density in a predetermined area, and generating digital multi-value data in correspondence with the judged density.

As described above, when a pattern division means and a density detection means are added to a character generator for generating a binary character pattern, a gray scale font having an arbitrary size or gray level can be generated.

It will be understood that the invention differs substantially from using a binary representation of the gray scale for each pixel of the image, because according to the encoding method used the number of bits set is monotonically related (i.e. directly proportional) to the image density at a pixel whereas a binary code is not monotonically related to the value it represents. It is believed that the monotonicity of the relationship enables the font to be scaled to expand or reduce its size whilst retaining the gray level information.

Claims

1. A pattern generation apparatus comprising:
storage means for storing binary bit map data;

judgment means for dividing an area of the bit map data stored in said storage means, and judging a density in a predetermined area; and

generation means for generating corresponding digital multi-value data in accordance with the density judged by said judgment means.

2. An apparatus according to claim 1, further comprising conversion means, having vector fonts, for converting desired data of the vector font into the binary bit map data.

3. An apparatus according to claim 1, further comprising printer means for receiving said multi-value data, converting said multi-value data into an analog image signal, and comparing the analog image signal with a triangular wave having a predetermined period to form a pulse-width modulated signal.

4. An apparatus according to claim 1, further comprising printing means for controlling ink injection in accordance with said multi-value digital data to express gradation.

5. A pattern generation method comprising the steps of:

generating corresponding binary bit map data based on desired one of vector font data;

dividing an area of the binary bit map data on a memory;

judging a density in a predetermined area; and

generating digital multi-value data in correspondence with the judged density.

6. A method according to claim 5, wherein the step of judging the density comprises the step of judging a distribution state of a pattern in the predetermined area.

7. A method according to claim 5, further comprising printer means for receiving said multi-value data, converting said multi-value data into an analog image signal, and comparing the analog image signal with a triangular wave having a predetermined period to form a pulse-width modulated signal.

8. A pattern generation method comprising the steps of:

generating multi-value digital data on the basis of vector data; and

converting the multi-value digital data into an analog image signal, and comparing the analog image signal with a triangular wave having a predetermined period to pulse-width modulate the analog image signal, thereby outputting a gradation pattern.

9. A method according to claim 8, wherein the multi-value digital data is obtained by generating a binary bit map pattern based on the vector data, and judging a density based on a distribution of the binary bit map pattern.

10. A pattern generation apparatus comprising: generation means for generating multi-value digital

data on the basis of vector data;

means for converting the multi-value digital data into an analog image signal, and comparing the analog image signal with a triangular wave having a predetermined period to obtain a pulse-width modulated signal; and

laser output means for outputting a gradation image based on the pulse-width modulated signal obtained by said converting means.

11. An apparatus according to claim 10, wherein said generation means converts the vector data into the binary bit map data, and judges a density based on a distribution of a bit pattern so as to generate the multi-value digital data.

12. An image processing apparatus including a store means in which the arrangement of stored data is such that successive bits relate to continuous spacial lines of the image, characterised in that a plurality of bits are allocated to represent a single image point and in that the number of said plurality of bits which are set relates monotonically to the density of the image at that point.

13. Apparatus according to claim 12 in which N bits are allocated to each image point, and each image point can exhibit N + 1 possible density levels, each density level corresponding uniquely to a given number of said bits being set.

14. A method of generating a digitally stored image representation comprising the steps of setting, for each image point, the value of a binary location within a memory in which an array of such locations are mapped onto corresponding image points, characterised in that it comprises also the steps of determining the density of the image at the point, and setting a predetermined number of bits proportional to the density within the memory.

15. A bit map image processing apparatus comprising means for expanding a bit map image so that a plurality of bits correspond to a given pixel.

FIG. 1

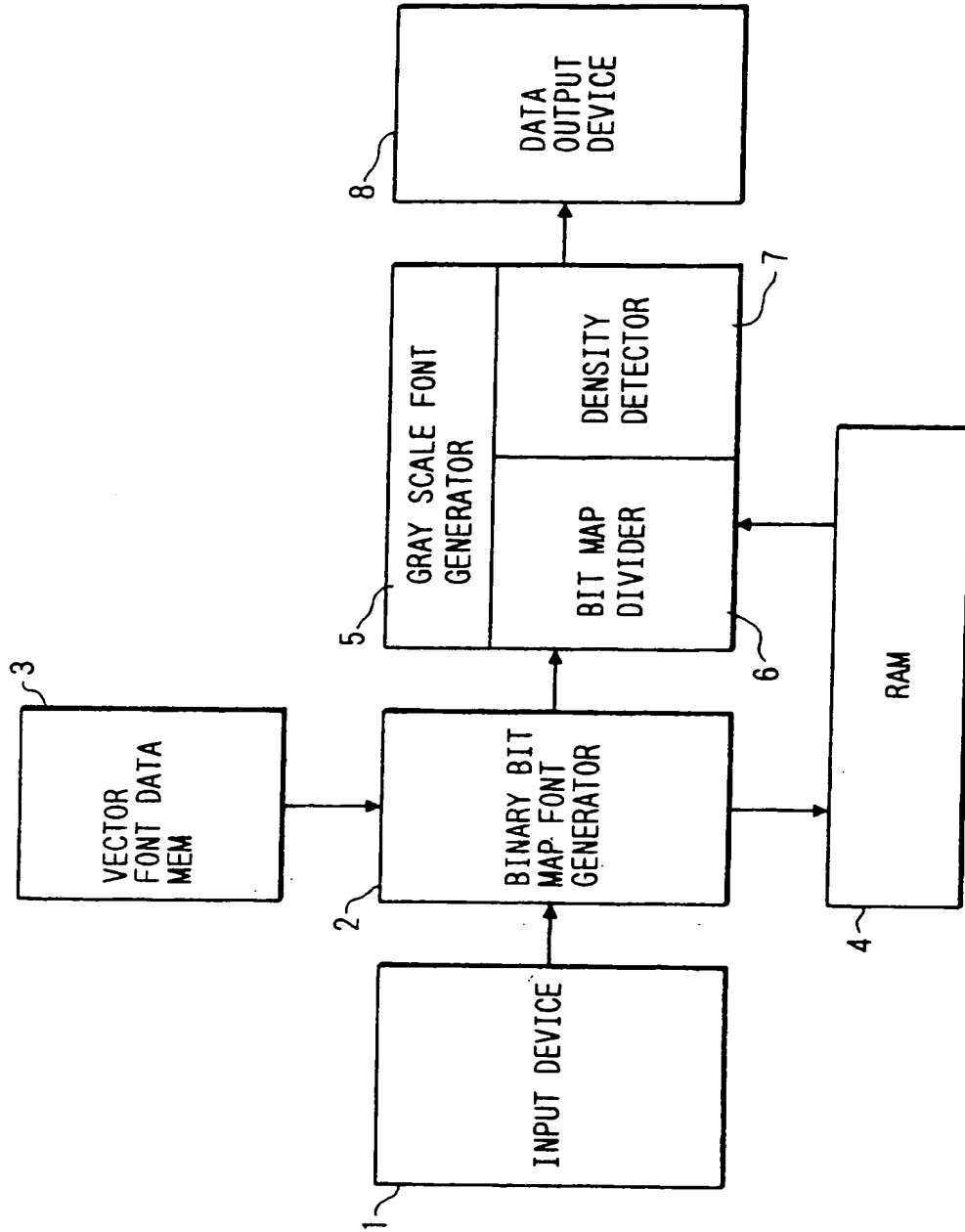


FIG. 2A



FIG. 2B

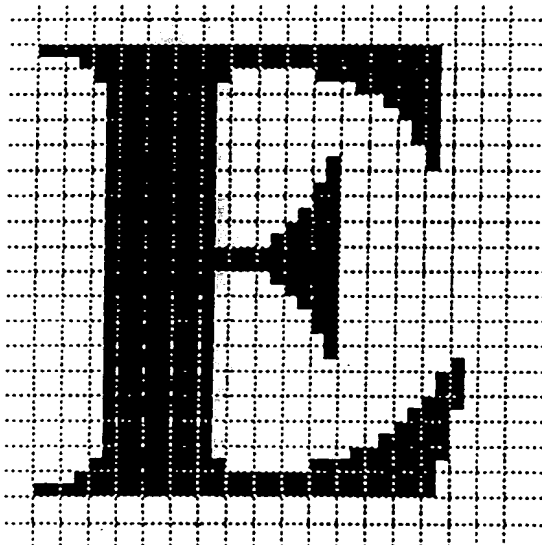
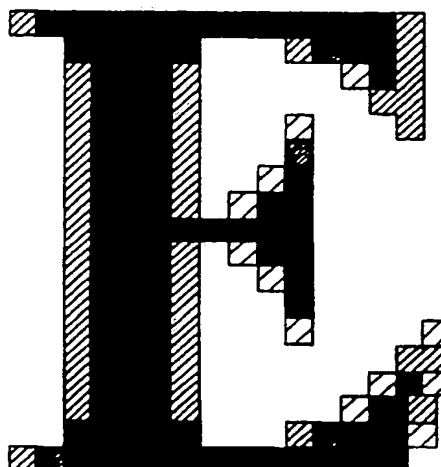


FIG. 2C



□ 25% GRAY

▨ 50% GRAY

■ 75% GRAY

FIG. 3

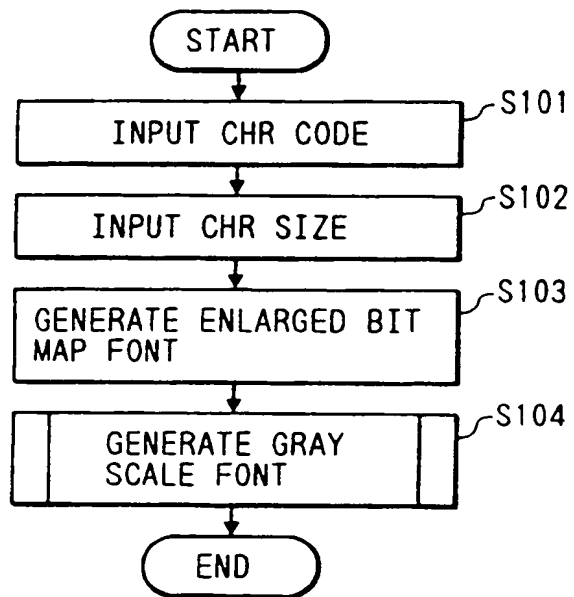


FIG. 4

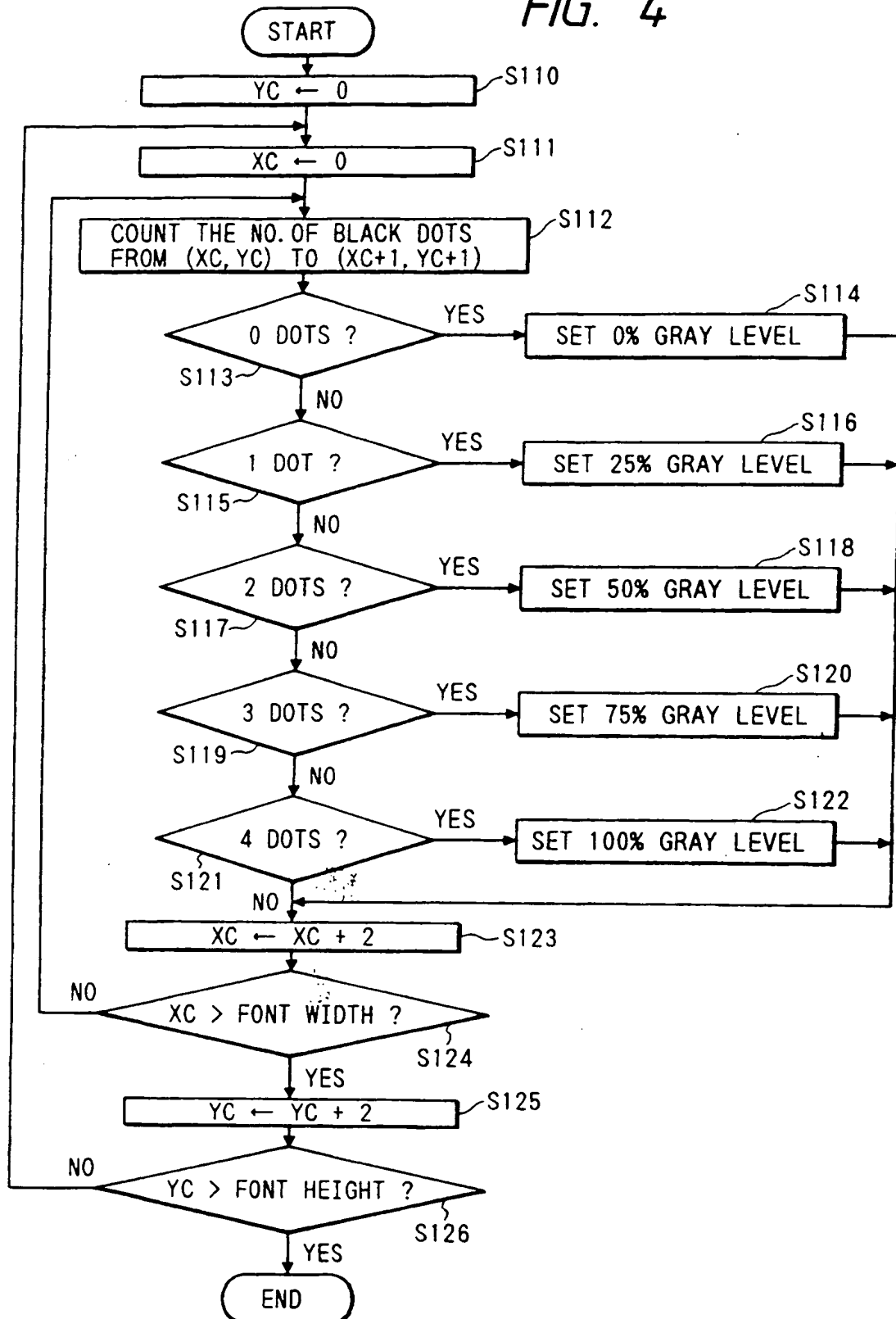
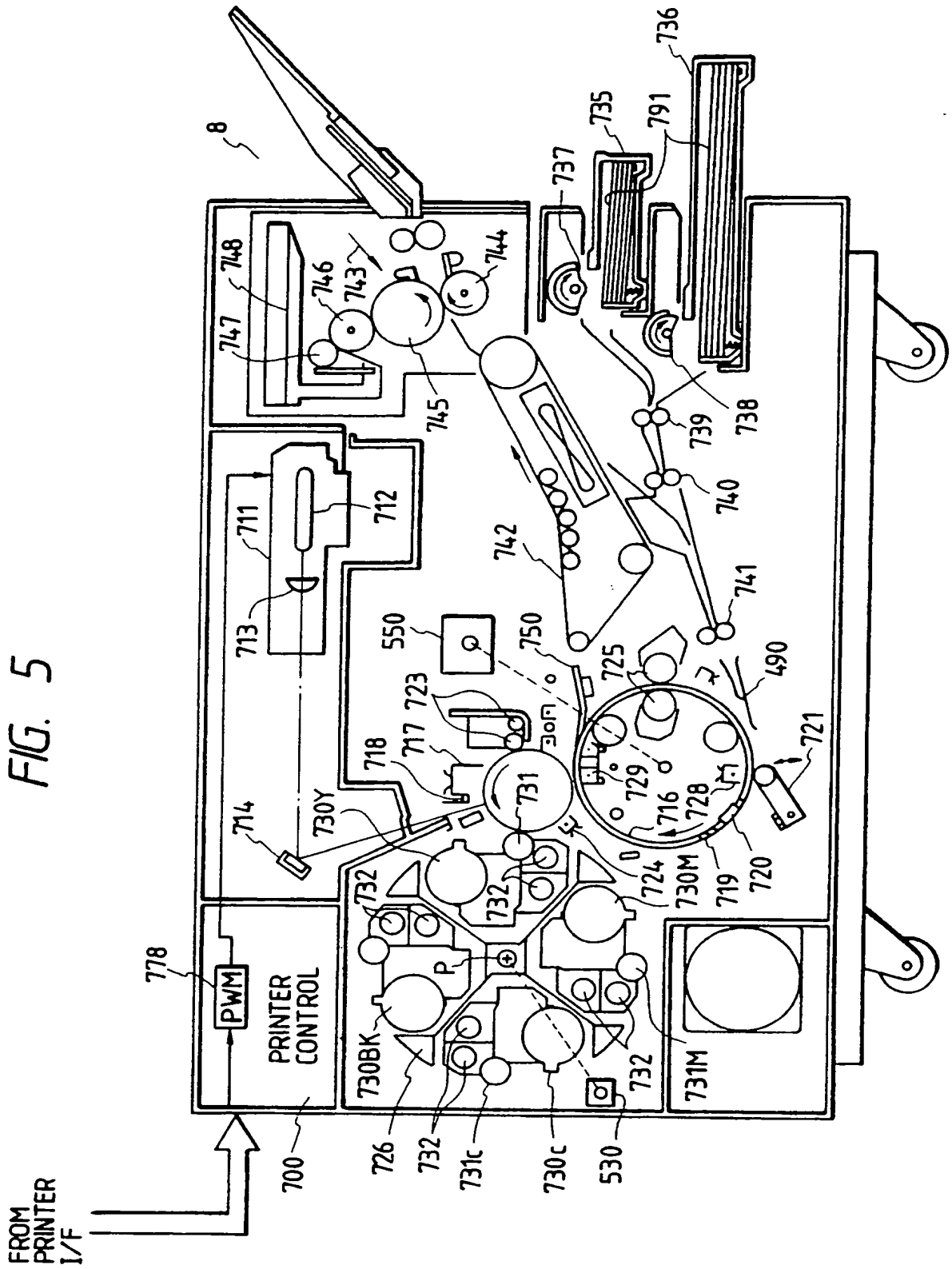


FIG. 5



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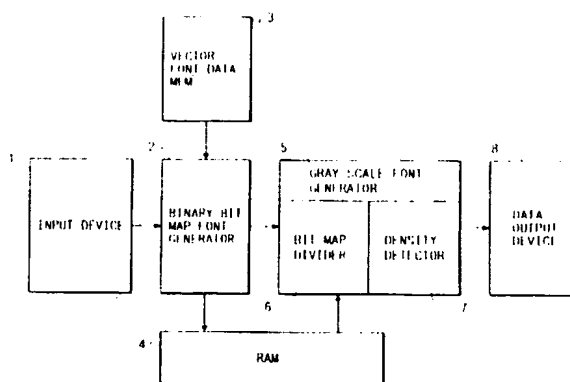
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DE GB(56) Date of deferred publication of the search report:
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London WC1R 5DJ(GB)(54) **Pattern generation method and pattern generation apparatus.**

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FIG. 1**EP 0 428 356 A3**



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EUROPEAN SEARCH REPORT

Application Number

EP 90 31 2321

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
X	EP-A-0 132 454 (DR.-ING. R. HELL GMBH)	1,12-14	G09G1/14
Y	* page 5, line 3 - page 9, line 18; figures 1-7	2-7,9	G09G5/24
A	---	15	
X	EP-A-0 215 664 (NHANCE DEVELOPMENT CORPORATION)	14	
Y	* page 19, paragraph 3 - page 20, paragraph 4; figures 2A-C *	2,5-11	
A	* page 25, paragraph 2 - page 26, paragraph 3; figure 6 *	1	
	* page 32, paragraph 2 - page 35, paragraph 1; figures 8,9 *		
Y	--- PATENT ABSTRACTS OF JAPAN vol. 012, no. 67 (M-673)2 March 1988 & JP-A-62 212 153 (CANON INC) * abstract *	3,7-11	
Y	--- PATENT ABSTRACTS OF JAPAN vol. 9, no. 77 (M-369)6 April 1985 & JP-A-59 207 265 (YOKOGAWA HIYUURETSUTO PATSUKAADO KK) 24 November 1984 * abstract *	4	
A	--- PATENT ABSTRACTS OF JAPAN vol. 13, no. 555 (M-904)11 December 1989 & JP-A-12 028 852 (CANON INC) 12 September 1989 * abstract *	10	TECHNICAL FIELDS SEARCHED (Int. Cl.5) G09G G06K
P,A	--- EP-A-0 360 402 (IBM CORP.) * page 2, line 18 - page 5, line 30; figure 1 *	15	
P,A	--- EP-A-0 386 995 (CANON K.K.) 12 September 1990 * column 5, line 1 - column 5, line 37; figures 1,8 *	3-4,7-8,10	

The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 27 JULY 1992	Examiner ZENDER J.J.
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document I : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

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